

Logic Design Interview Questions

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More recently, tech giants like Airbnb and Facebook have adopted logic puzzle questions to find the right software developers to keep them ahead of the competition. With this in mind, Aaron Wallis Sales Recruitment has collated a series of 3 great logic puzzle questions for employers to work into their recruitment processes: 1. Newcastle rain. Question:

[Logic-Based Interview Questions For Employers - Minutehack](#)

These are some of the most commonly asked questions in digital logic design for VLSI related jobs Finite state machines mealy and moore machine. Sequence detection like 10100,01011 etc using both mealy and moore machine and their verilog implementation Combinational circuits like carry look ahead adder, Multiplexers, decoder, Priority encoders etc

[What are Digital Logic Design interview questions? - Quora](#)

These are easy to design. These are difficult to design. 3. The clock input is not required. The clock input is required. 4. In this, the memory units are not required. In this, the memory units are required to store the previous values of inputs. 5. Example: Mux, Demux, encoder, decoder, adders, subtractors. Example: Shift registers, counters.

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Introduction to Digital Electronics Interview Questions. Digital electronics are electronics that operate on digital signals where digital electronic circuits are made from a series of logic gates by assembling them. Each logic gate performs a function based on Boolean values with the help of signals from logic gates.

[10 Essential Digital Electronics Interview Questions ...](#)

[Most Popular Questions collections below, first is Digital Basics: Give two ways of converting a two input NAND gate to an INVERTER. Solved here; Drive XOR from NAND gates. Drive XOR from NOR gates. Solved here; Discuss full custom logic to build 4 bit binary-adders.](#)

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A Computer Science portal for geeks. It contains well written, well thought and well explained computer science and programming articles, quizzes and practice/competitive programming/company interview Questions.

[Digital Electronics and Logic Design Tutorials - GeeksforGeeks](#)

basic questions on logic design, difference between latch and flipflop, setup time,hold time, violations and how to avoid violations,built and, or,nand gates using MUXs Be the first to answer this question Logic Design Engineer at Samsung Austin Semiconductor was asked... Feb 18, 2017

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Digital Logic Design Interview Questions - XpCourse Logic-based interview puzzles are becoming increasingly popular in recruitment processes to help employers find the brightest candidates and gain insight into a candidate ' s thought process. Brain teasers are particularly popular in the tech sector, where

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I applied online. The process took a week. I interviewed at Cirrus Logic in July 2012. Interview. Was schedule a phone interview. The manager of the team took the initial phone interview. He started asking questions on Computer Architecture , super scalar , VLIW and ILP techniques.

[Cirrus Logic Design Verification Engineer Interview Questions](#)

Description. Digital RTL design and Verilog interview questions is an initiative to help students/professionals who have basic knowledge of digital design and Verilog knowledge to quickly ramp up for an interview . The course is structured as 6 part series addressing the following interview question/concepts . Synchronizers interview questions part1 - MTBF , 2FF synchronizer , 3FF synchronizer.

[Digital RTL design and Verilog interview questions | Udemy](#)

High Level Design (HLD) and Low Level Design (LLD) of Real Time Projects Most developers struggle with the system design interview, partly because of their lack of experience in developing large-scale systems and partly because of the lack of complete understanding scalable design components This Course is complete guide to master in System Design Interview

[Cracking the Coding Interview | System Design Interview ...](#)

Questions on bus architecture, synthesis, protocols, verilog, and certain design problems. 3. 1 major design question by each panes, takes 15-20 mins to solve; related to logic design--flip-flops, decoders, etc.

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